

17 January 2019

VACANCY NOTICE
RF/Mixed Signals IC Senior Design Engineer
(Seniority level: Experienced)

Saphyrion Sagl, among the European leaders in rad-hard IV for space-borne electronic applications, is looking for an experienced engineer to address new and challenging projects in the domain of rad-hard IC design. This job opens the route to the possibility of working in close contact with some of the most ambitious space missions planned in EU and US.

The role requires a significant skill in the management of the design of complex RF/Mixed signal IC architectures, ranging from L-band ICs (mainly for GNSS receivers) to complex solutions for integrated RF functionalities in the Ka band. To give a successful answer to these requirements in the context of a small but highly experienced company, the new resource should be able to establish an effective interaction with the existing team and to have a clear understanding of the general scope of the project, also beyond the specific tasks allocated to him/her. As an example, the individual should be able, whenever needed, to show command also of various aspects of practical electronics engineering for setup of ASIC test benches, to organize and conduct successfully campaigns of measurement of ICs performances at external facilities (irradiation laboratories) et.c.

The engineer should master the main CAD tools used in microelectronics (e.g. the CADENCE suite) and have an extensive knowledge of the issues encountered in integrated RF design at high frequencies. A previous experience of 3-5 years in similar activities will be particularly appreciated.

Basic Qualifications:

- (1) Significant experience in complex microelectronics design
- (2) Very good command of CADENCE and other popular CAD tools used in integrated circuit design
- (3) Significant experience in radiofrequency design at microwave bands up to 30GHz
- (4) Knowledge of the physics of radiation interaction with semiconductor materials, and of its main effects on electronic circuits (damage, latch-up etc.)
- (5) Acquaintance with the most important processes used in the semiconductor industry (CMOS, BiCMOS, SOI, SiGe, GaAs)
- (6) Excellent knowledge of English mandatory; German or other main European languages- a plus

Desired Skills:

- (1) Experience in the management of ESA bids and previous experience in the development of ESA contracts
- (2) Experience with digital signal processing (filtering, decimation etc.)
- (3) Experience in GNSS technology and positioning algorithms
- (4) Experience in semiconductor testing and packaging
- (5) Familiarity with digital ASIC design and multi-chip module solutions

Saphyrion is located in the southern (Italian-speaking) part of Switzerland, near the Lake of Lugano, in a region highly considered because of its natural beauties and mild climate. The Italian town of Milan is only 70 km away.

The compensation package is in line with the standard of Swiss high-tech industry, and can satisfy even the most qualified candidates.

Interested candidates are invited to submit their CV and other support documents (if any) to:

contact@saphyrion.ch