

23 January 2023

## VACANCY NOTICE

# Analog IC Senior Design Engineer

(Seniority level: Experienced)

Saphyrion Sagl, among the European leaders in rad-hard IC for space-borne electronic applications, is looking for an experienced engineer to address new and challenging projects in the domain of rad-hard IC design. This job opens the route to the possibility of working in close contact with some of the most ambitious space missions planned in EU and US, with particular reference to build an European hub for space qualified ICs design.

The role requires a significant skill in the management of the design of complex Analog and possibly RF/Mixed signal ICs, ranging from low frequency operational circuits up to V/W band on both Bi-CMOS and pure CMOS silicon technologies. Experience in III-V semiconductor process such as GaAs will be a plus. To give a successful answer to these requirements in the context of a small but highly experienced company, the new resource should be able to establish quickly an effective interaction with the existing team, and to have a clear understanding of the general scope of the project, also beyond the specific tasks allocated to him/her. As an example, the individual should be able, whenever needed, to show command also of various aspects of practical electronics engineering for setup of ASIC test benches, to organize and conduct successfully campaigns of measurement of ICs performances at external facilities (irradiation laboratories) etc.

The new hire should master the CADENCE EDA tools for analog IC design and possibly have experience in transistor level RF/Mixed-signal IC design. The engineer will be asked to perform layout design of the assigned block, as well as participate to lab testing activity of the IC.

A previous experience of 3-5 years in the design of Analog and possibly RF/Mixed-signal IC design is mandatory.

### Basic Qualifications:

1. Masters - Electrical Engineering (Ph.D a plus).
2. Significant experience in complex Analog and RF/mixed-signal IC design.
3. Experience in radiofrequency design (appreciated).
4. Excellent knowledge of English mandatory; German or other main European languages will be a plus.

### Desired Skills:

1. Professional experience using CADENCE analog IC design EDA tools.
2. Circuit design skills, and experience performing analysis and simulation of both linear and non-linear circuits such as phase-locked loops, vector modulators/demodulators, oscillators, mixers, filters, low noise and power amplifiers.
3. Acquaintance with the most important processes used in the semiconductor industry (CMOS, BiCMOS, SOI, SiGe). Experience in III-V semiconductor process such as GaAs will be a plus.
4. Experience in PCB design.

5. Experience in EM modelling tools.
6. Experience in semiconductor production flow (packaging, testing, qualification) will be a plus.

Saphyrion is located in the southern (Italian-speaking) part of Switzerland, near the Lake of Lugano, in a region highly considered because of its natural beauties and mild climate. The Italian town of Milan is only 70 km away.

The compensation package is in line with the standard of Swiss high-tech industry, and can satisfy even the most qualified candidates.

Interested candidates are invited to submit their CV and other support documents (if any) to:

[contact@saphyrion.ch](mailto:contact@saphyrion.ch)

**Please note that this vacancy notice requires well-specified and peculiar technical skills that are needed in a relatively small company, and will be immediately exploited in ongoing and recently acquired projects. Please crosscheck that your expertise matches to a good extent the required profile before answering. We are so sorry to decline non-resonant candidatures!**